ABSTRACT OF THE DISCLOSURE

Graphics processing circuitry includes a graphics processing unit, a system interface coupled to the graphics processing unit, and a graphics memory coupled to the graphics processing unit. The graphics memory holds an image data block, a vertex data block, and rendering plane definitions. The image data block stores image data entries for at least one ultrasound imaging beam and the vertex data block stores vertex entries that define rendering shapes. The graphics processing unit accesses the image data entries and vertex entries to render, from back to front or back to front using alpha compositing, a volume according to the rendering plane definitions.